

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Number: 7,299,029
Issued: 11/20/2007
Name of Patentee: Joseph S. Elder, et al.
Title of Invention: METHOD OF OPERATING RADIO RECEIVER IMPLEMENTED IN A SINGLE CMOS INTEGRATED CIRCUIT

Attention: Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT
FOR APPLICANT'S MISTAKE (37 C.F.R. section 1.323)

1. It is noted that errors appear in this patent of a typographical nature or character, as more fully described below. They occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require re-examination. A certificate of correction is requested.
2. Attached hereto, in duplicate, is Form PTO-1050, with at least one copy being suitable for printing.
3. The exact page and line number where the error occurs in the application file are:

In the Office Action dated November 16, 2006, the Examiner acknowledged all references cited in the Information Disclosure Statement filed January 12, 2004. However, four (4) of these references are absent from the issued patent.

In the Amendment in response to the Office Action dated April 4, 2007;

Page 4, Claim 12, 13th line
Page 10, Claim 33, 11th line

4. Please send the Certificate to the undersigned at Customer No.: 22888
5. Please pay the fee required by 37 C.F.R. § 1.20(a), as follows:

Authorization is hereby made to charge the amount of \$100.00 to Deposit Account No. 50-0574. Charge any additional fees required by this paper or credit any overpayment in the manner authorized above. A duplicate of this paper is attached.



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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,299,029
DATED : 11/20/2007
INVENTOR(S) : Joseph S. Elder, et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Page 2, OTHER PUBLICATIONS, insert; --

Khorram, S. et al., "A CMOS Limiting Amplifier & Logarithmic Signal-Strength Indicator," Integrated Circuits & Systems Laboratory, Electrical Engineering Department, University of California, Los Angeles, 19 pages.

Rofougaran, A., et al., "A 900 MHz CMOS LC-Oscillator with Quadrature Outputs," presented at the 1996 Int'l. Solid-State Circuits Conference, 10 pages.

Rudell, Jacques C., "CMOS VLSI Frequency Translation for Multi-Standard Wireless Communication Transceivers," Qualifying Examination, University of California, Berkeley, April 8, 1996, pp. 1-24.

Chang, Glenn et al., "An Agile, Low-Power CMOS Digitally Synthesized 0-13 MHz Sinewave Generator," Integrated Circuits & Systems Laboratory, Electrical Engineering Department, University of California, Los Angeles, 14 pages. --

Column 17, line 6, replace "superhetrodyne" with -- superheterodyne -- .

Column 19, line 2, replace "superhetrodyne" with -- superheterodyne -- .

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PATENT NO. 7,299,029
No. of additional copies: 0

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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